



Attorney Docket No. T2147-908627

Patent Application

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

First Named Inventor: KASZYNSKI, ANNE

Art Unit: 2128

Appln. No.: 10/627,976

Examiner: Alhija, S.A.

Filed: July 28, 2003

Confirmation No.: 4095

FOR: Method For Functional Verification Of An
Integrated Circuit Model In Order To Create A
Verification Platform, Equipment Emulator
And Verification Platform

McLean, Virginia
January 3, 2007

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action of October 3, 2006, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Amendments to the Drawings begin on page 9 of this paper.

Remarks begin on page 10 of this paper.